

Electronic Patent Application Fee Transmittal

| Application Number: | 10574030 | | | |
|---|---|----------|--------|----------------------|
| Filing Date: | 27-Mar-2006 | | | |
| Title of Invention: | Two-transistor memory cell and method for manufacturing | | | |
| First Named Inventor/Applicant Name: | Robertus Theodorus Van Schaijk | | | |
| Filer: | Robert J. Crawford/Kelly Davis | | | |
| Attorney Docket Number: | NL03 1167 US1 | | | |
| Filed as Large Entity | | | | |
| U.S. National Stage under 35 USC 371 Filing Fees | | | | |
| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
| Basic Filing: | | | | |
| Pages: | | | | |
| Claims: | | | | |
| Miscellaneous-Filing: | | | | |
| Petition: | | | | |
| Patent-Appeals-and-Interference: | | | | |
| Post-Allowance-and-Post-Issuance: | | | | |
| Extension-of-Time: | | | | |
| Extension - 1 month with \$0 paid | 1251 | 1 | 130 | 130 |

| Description | Fee Code | Quantity | Amount | Sub-Total in USD(\$) |
|-------------------|----------|----------|--------|----------------------|
| Miscellaneous: | | | | |
| Total in USD (\$) | | | | 130 |